

CERTIFICATE OF ELECTRONIC TRANSMISSION

I hereby certify that this correspondence is being electronically transmitted to United States Patent and Trademark Office on 04 October 2006.

/Kathryn Marley/
Kathryn Marley

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of inventor(s):

Shu Sing Liao et al.

Application No. **10/801,954**

Confirmation No. **2359**

Filing Date: **16 March 2004**

Title: **Method and System for Analyzing
Defects of an Integrated Circuit
Wafer**

Group Art Unit: 2823

Examiner: Julio J. Maldonado

CUSTOMER NO. 22470

MAIL STOP AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This RESPONSE A is in reply to the Office action mailed 19 September 2006, in the form of a Restriction Requirement.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 10 of this paper.

///